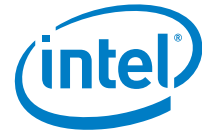


Intel® Quark™ SE Microcontroller C1000 Development Platform

Schematics

November 2016



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Revision History

Date	Revision	Description
November 2016	001	Initial release.

INTEL QUARK SE MICROCONTROLLER C1000

DEVELOPMENT PLATFORM

DEV KIT

J12658-105 PB
J29379-500 PBA
J29378-500 TA

EVAL KIT

J12658-105 PB
J35534-500 PBA
J35533-500 TA

LB5
LABEL

SERIAL NUMBER

A88430-001

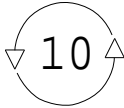
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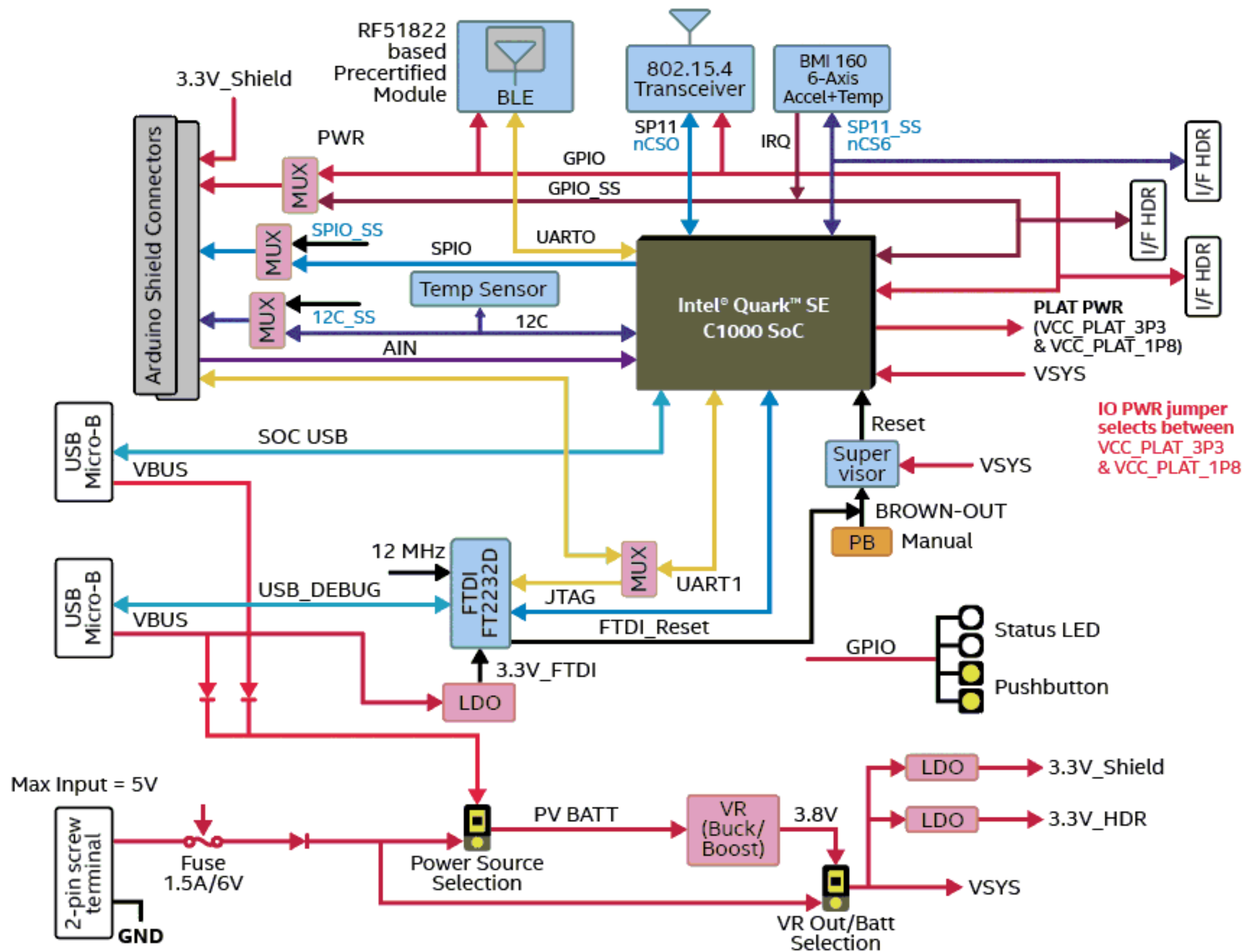


LABEL_CHINA_ROHS_10



INTEL CORPORATION 2111 NE 25TH AVENUE HILLSBORO, OREGON 97124-5961				TOOLS OPTIONS CUSTOM VARIABLES	
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SHEET SIZE:		<SHEETSIZE>		PBA NO:	<PBANO>
DATE:		Fri Nov 25 09:46:59 2016		REVISION:	5.0.0
				PAGE:	1 OF 22

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PAGE	SHEET NAME	PAGE	SHEET NAME
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2	TABLE OF CONTENTS		
3	BLOCK DIAGRAM		
4	ATP 1 OF 2		
5	ATP 2 OF 2		
6	IO HEADERS		
7	ARDUINO HEADERS		
8	LEDS		
9	SWITCHES		
10	MUXES		
11	SENSORS		
12	RADIO 15.4		
13	RADIO BLUETOOTH		
14	ATP USB		
15	FLASH		
16	VOLTAGE REGULATOR		
17	POWER SELECT		
18	POWER DISTRIBUTION		
19	FTDI 2232D		
20	FTDI ISOLATION		
21	RESET LOGIC		
22	CRYSTALS		

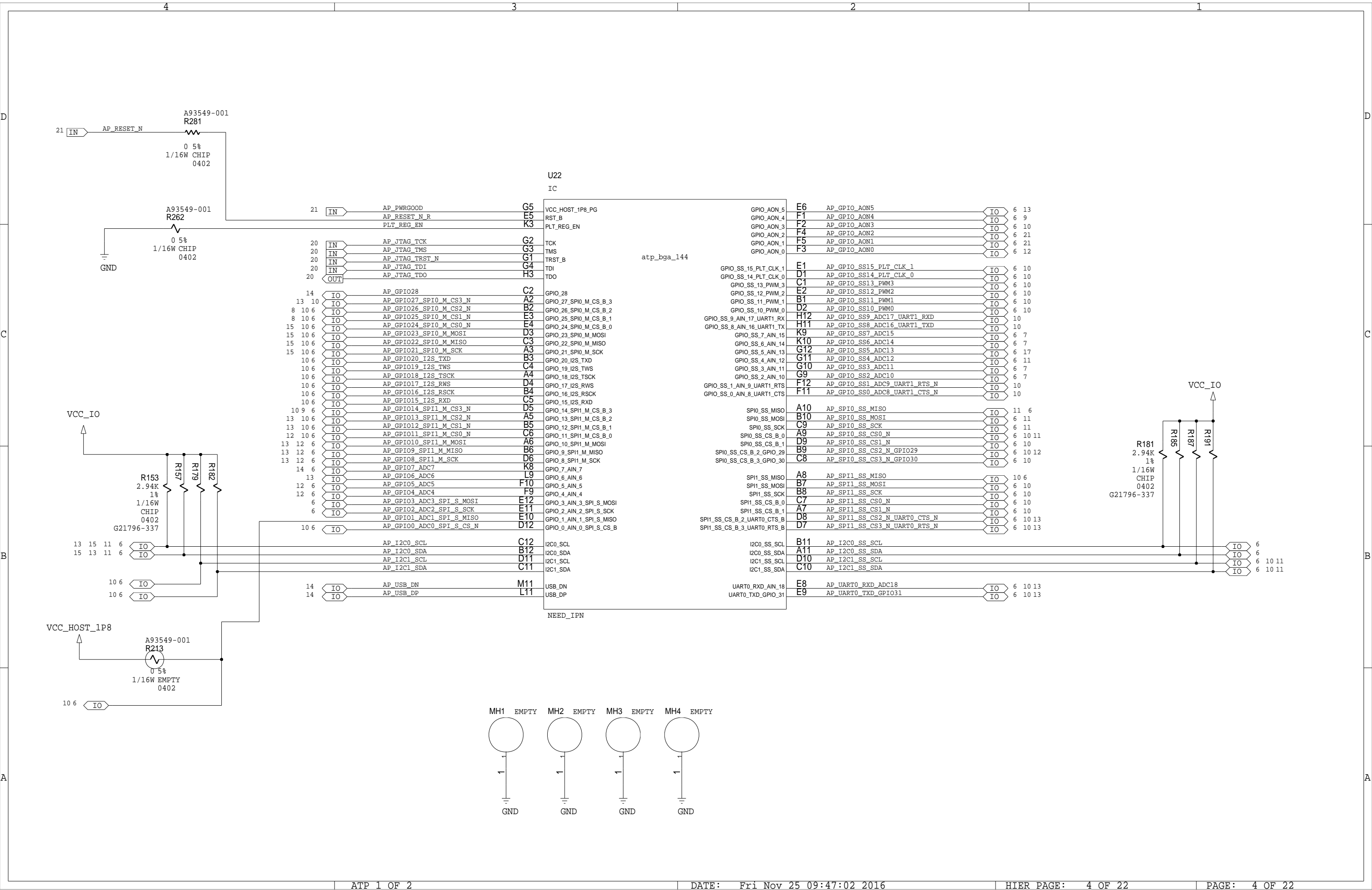


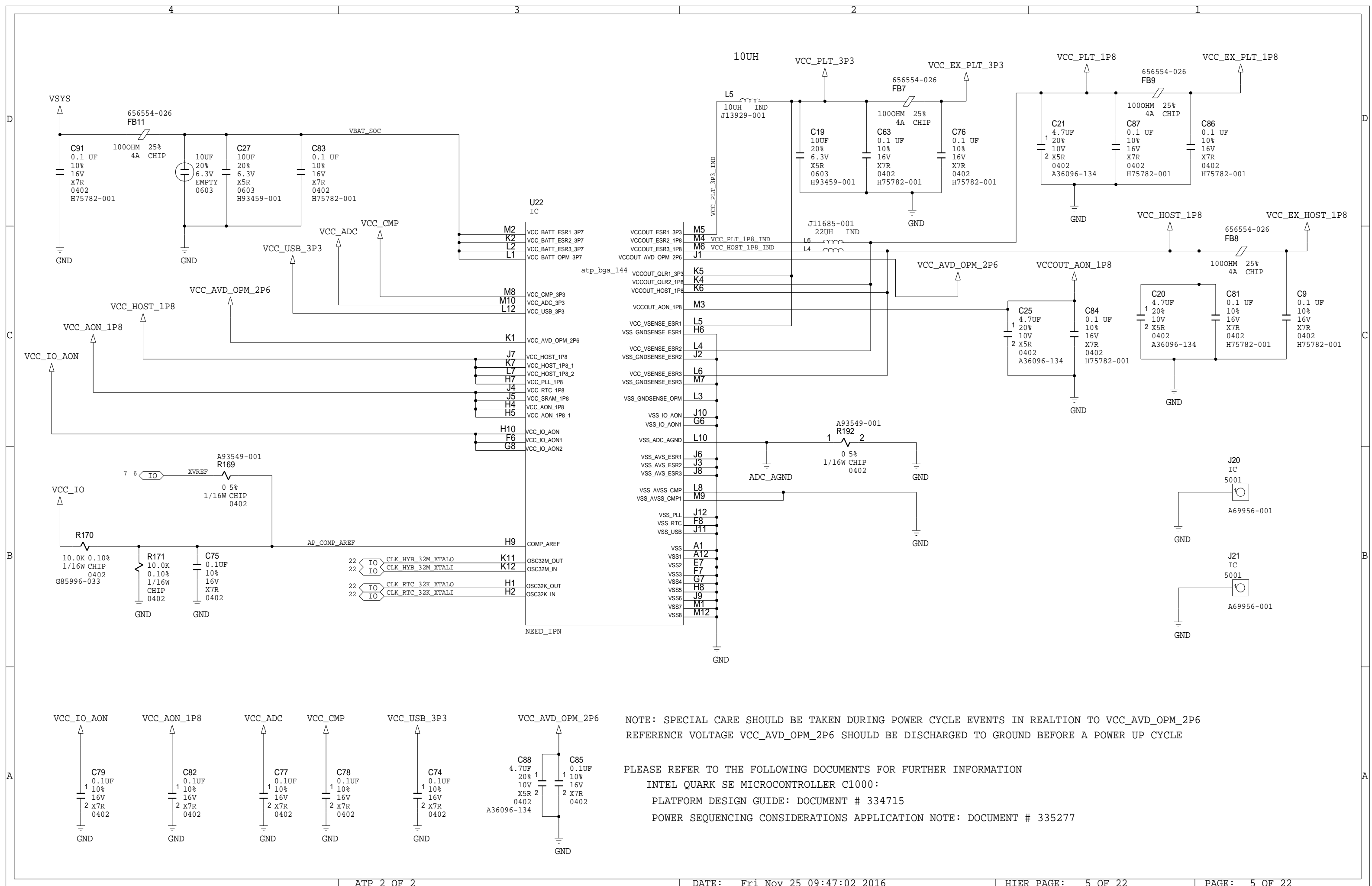
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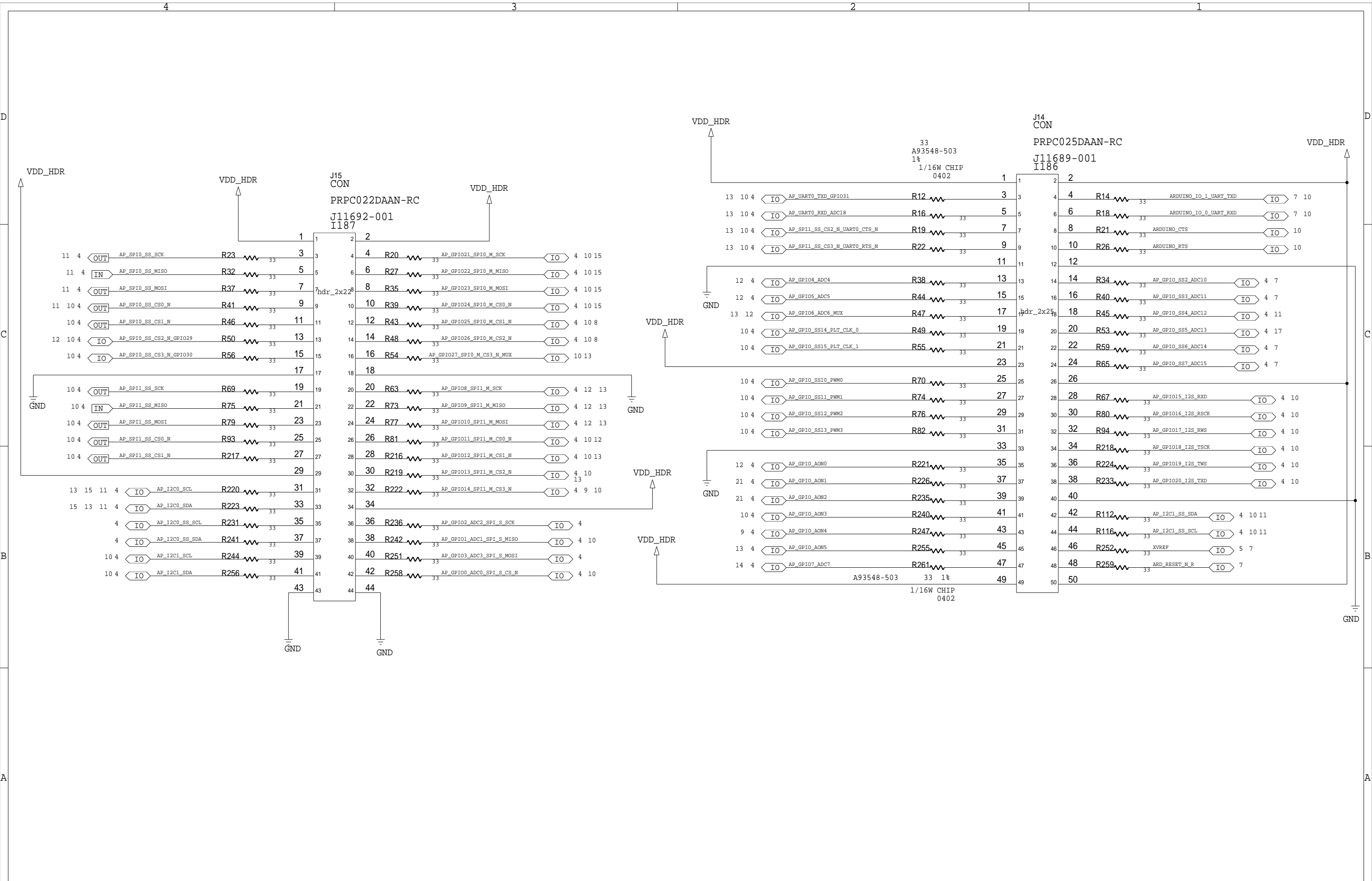
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2111 NE 25TH AVENUE
HILLSBORO, OREGON 97124-5961

TOOLS | OPTIONS | CUSTOM VARIABLES

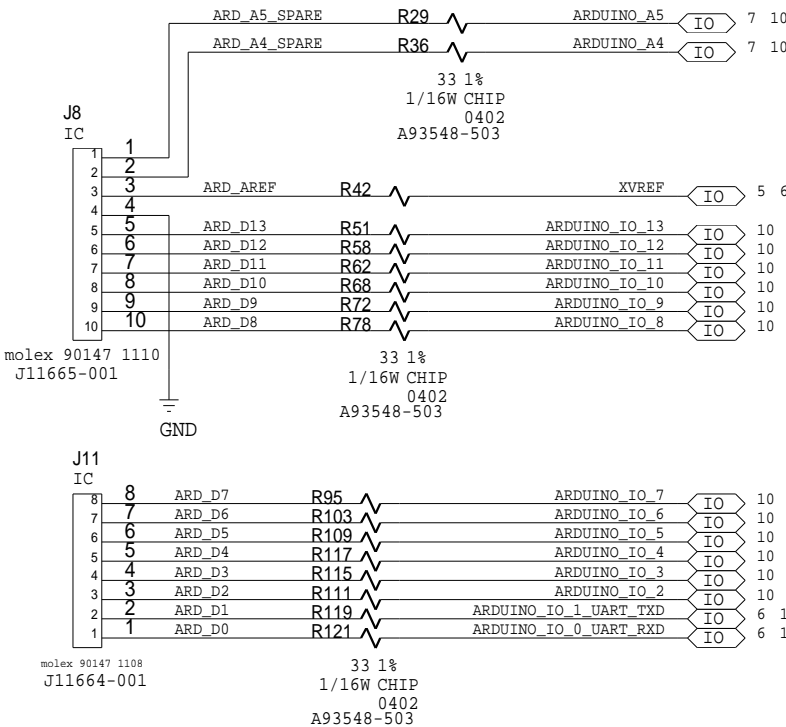
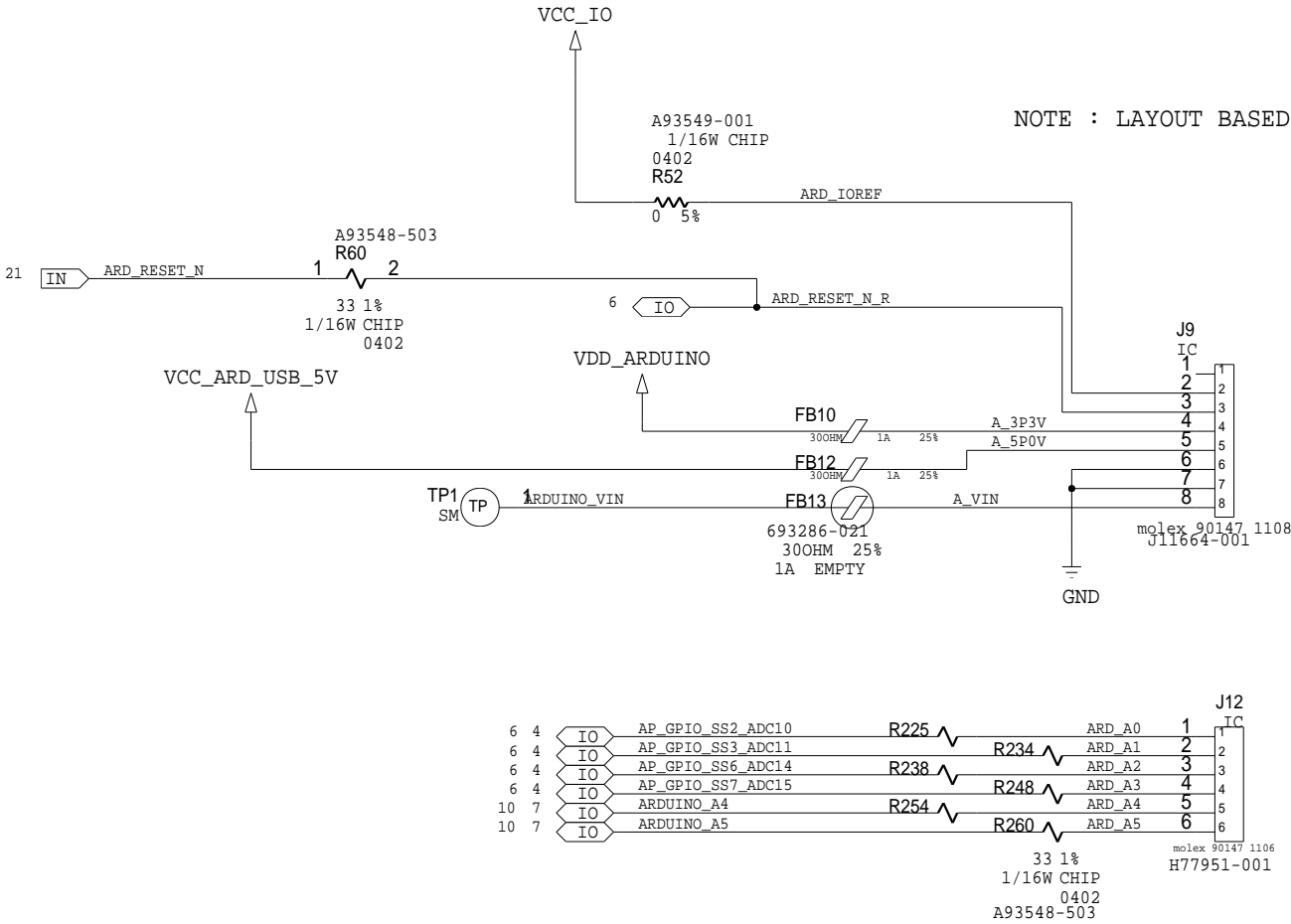
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REVISION:	5.0.0	PAGE:	3 OF 22

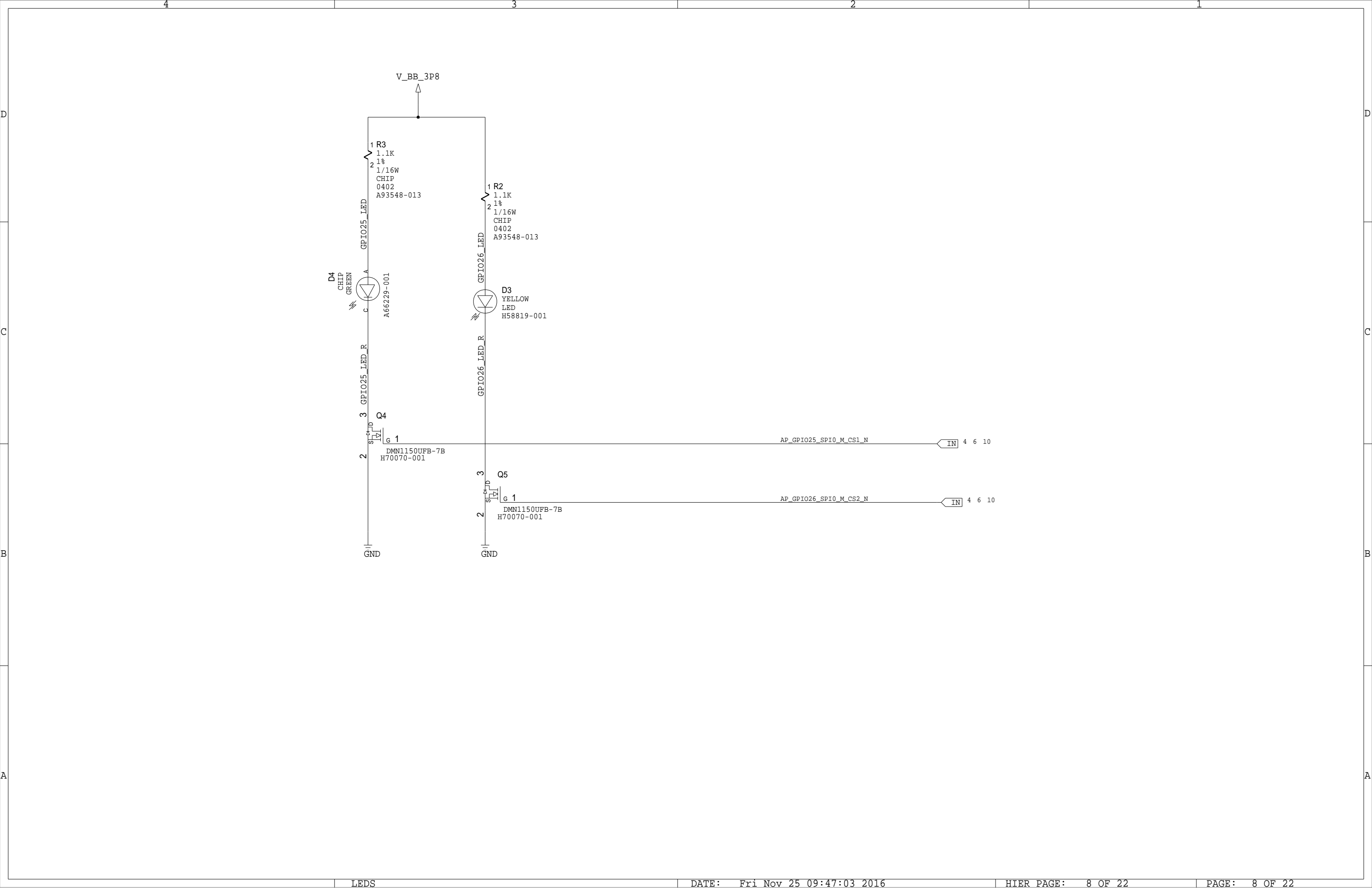


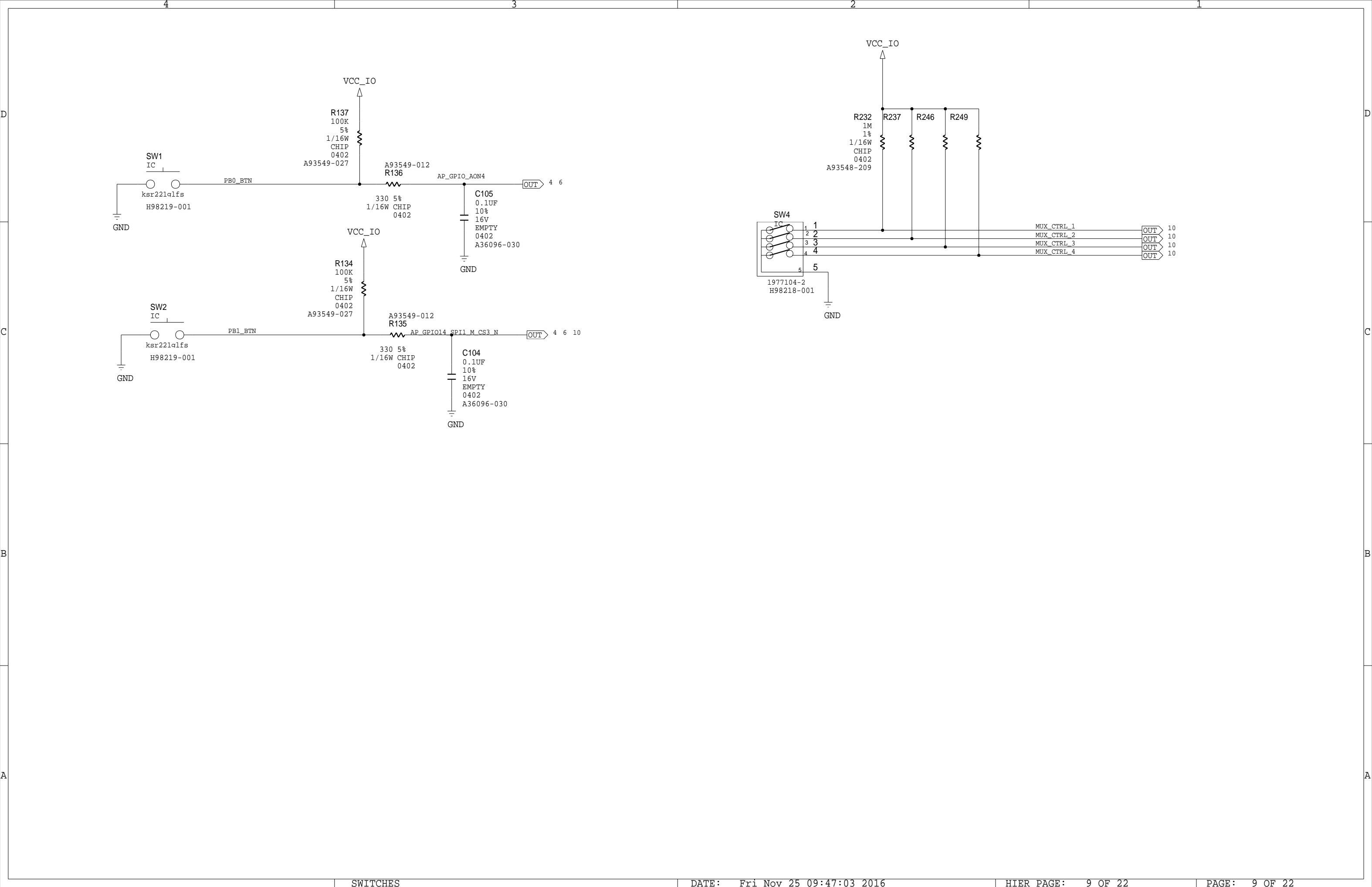


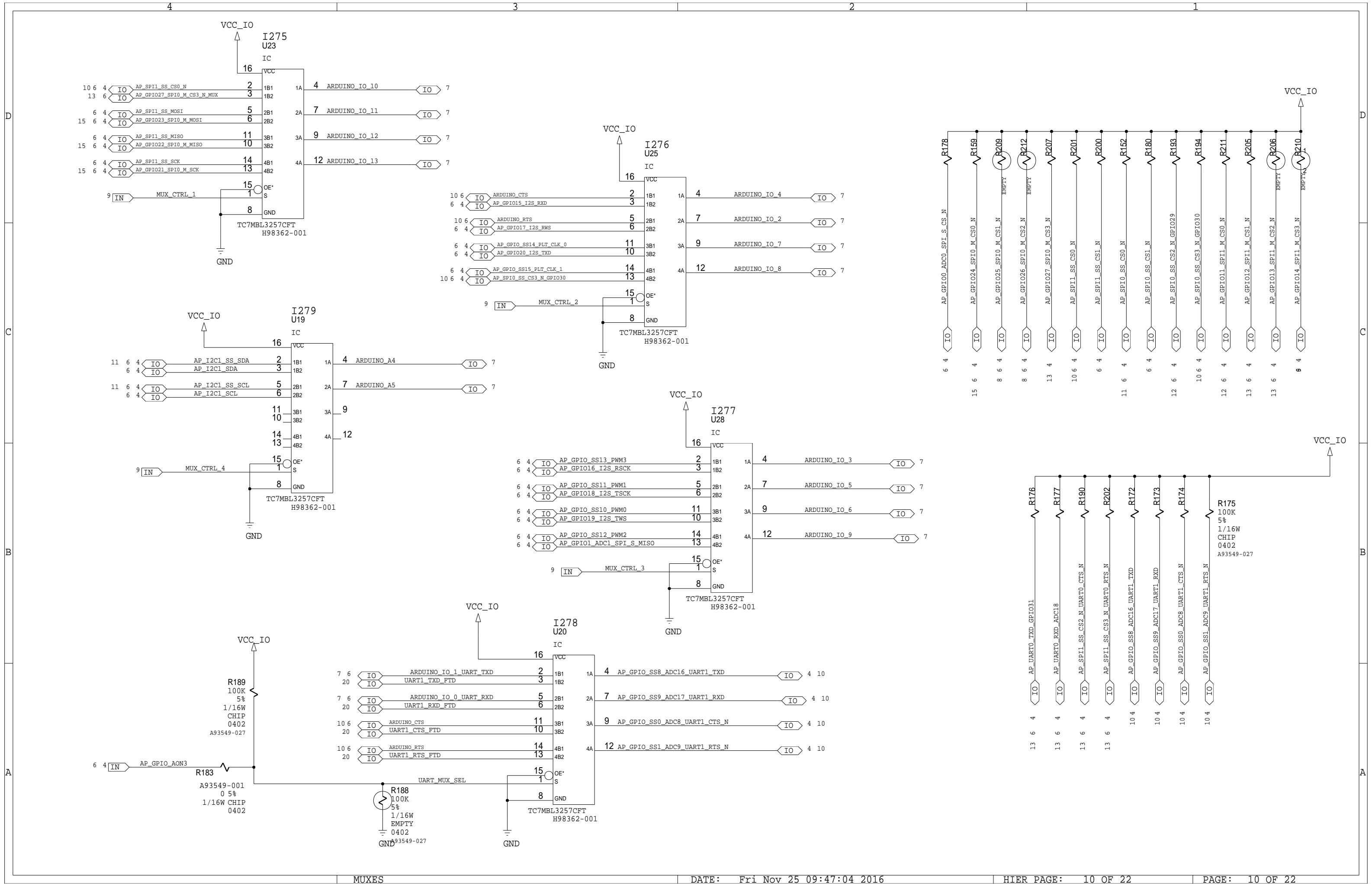


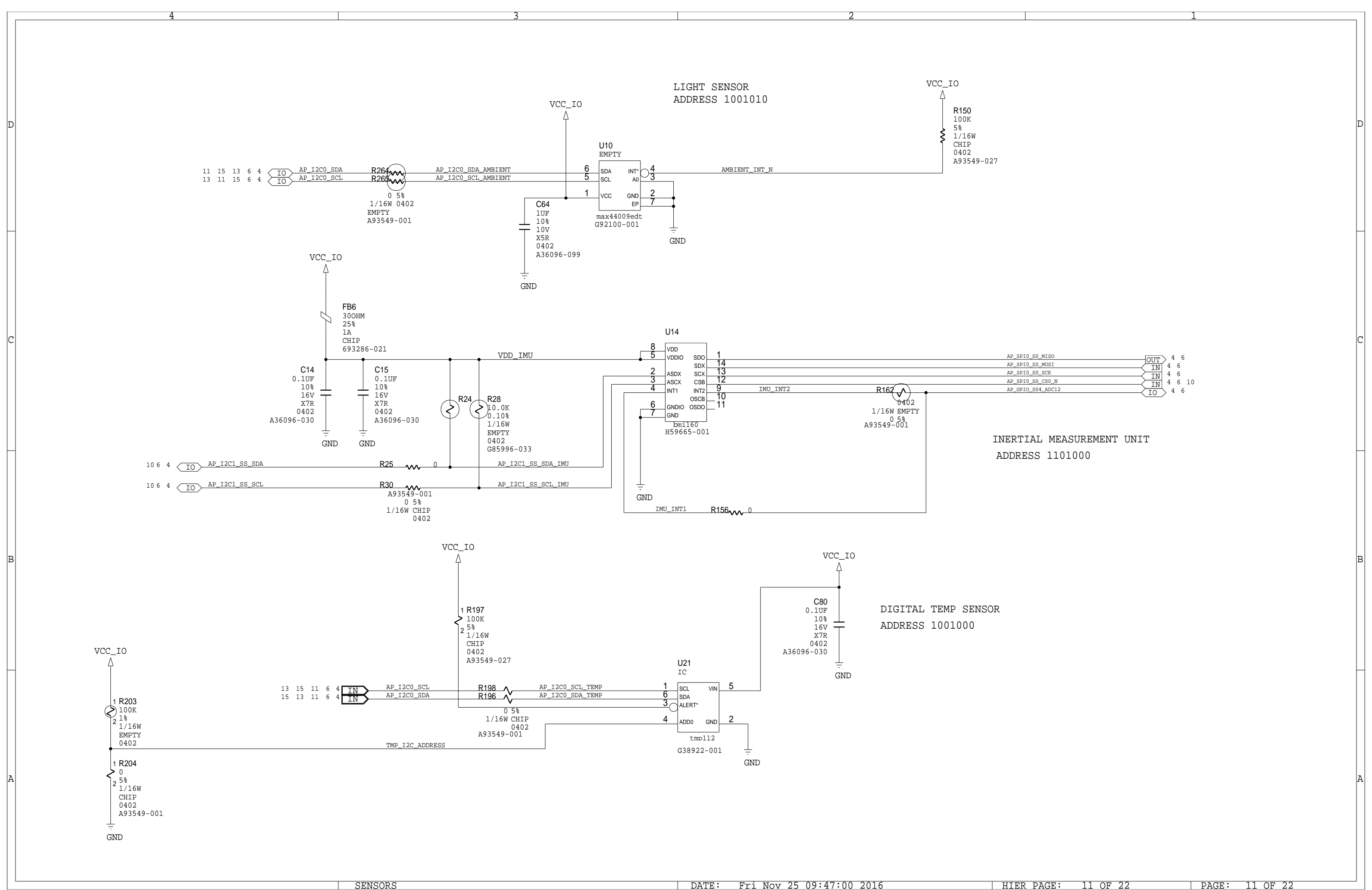
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WHEN USB POWER IS SUPPLIED

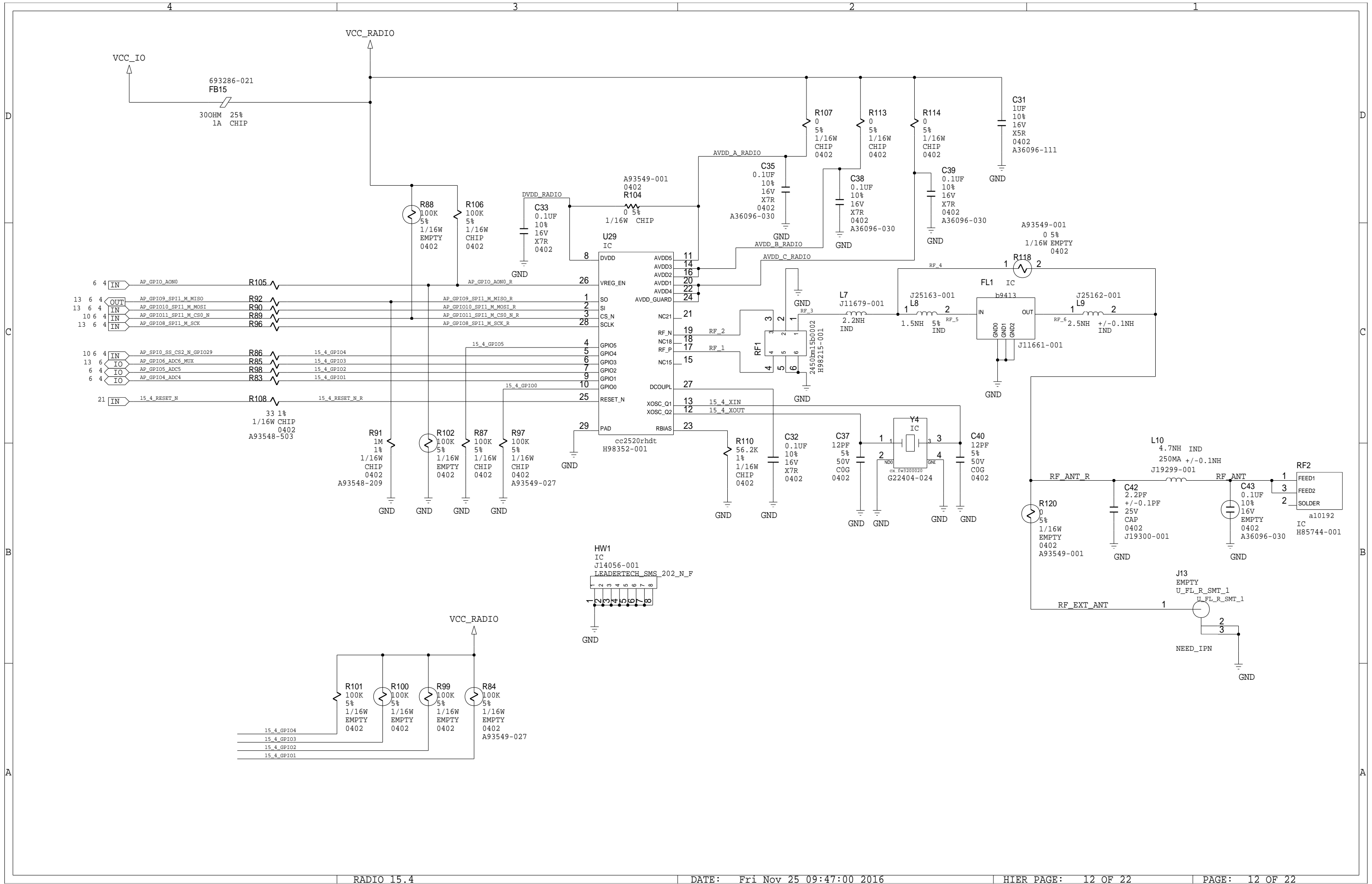












D

C

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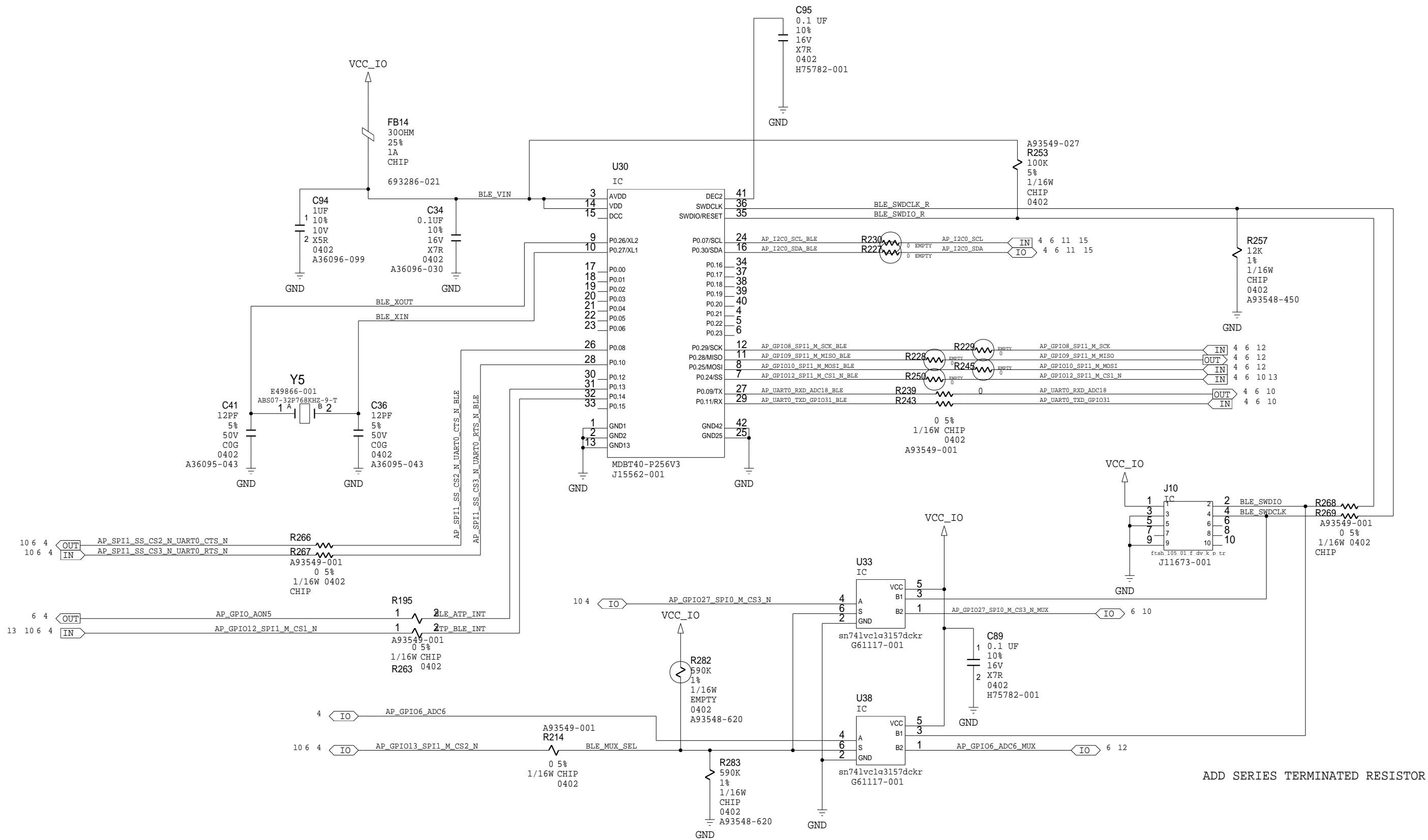
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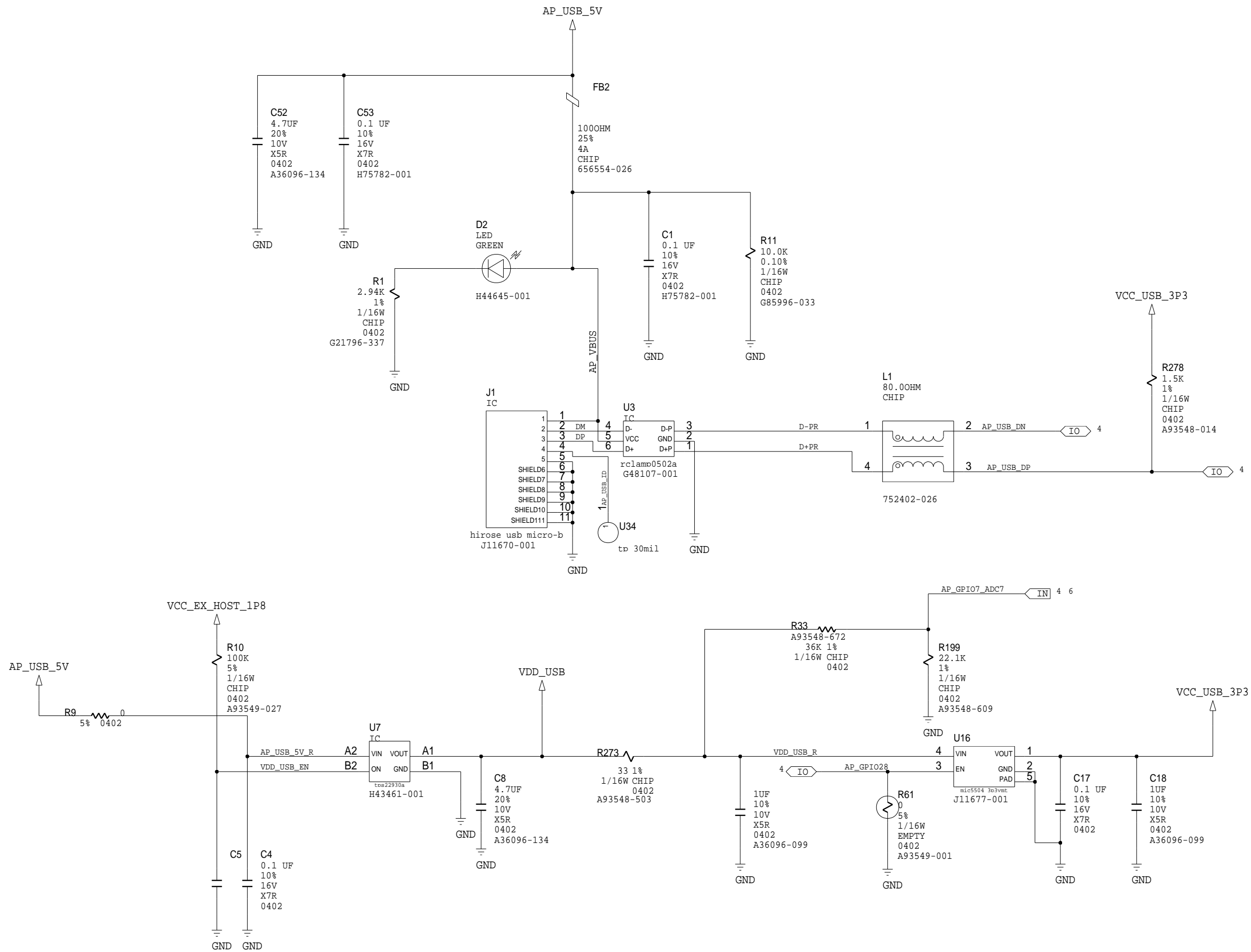
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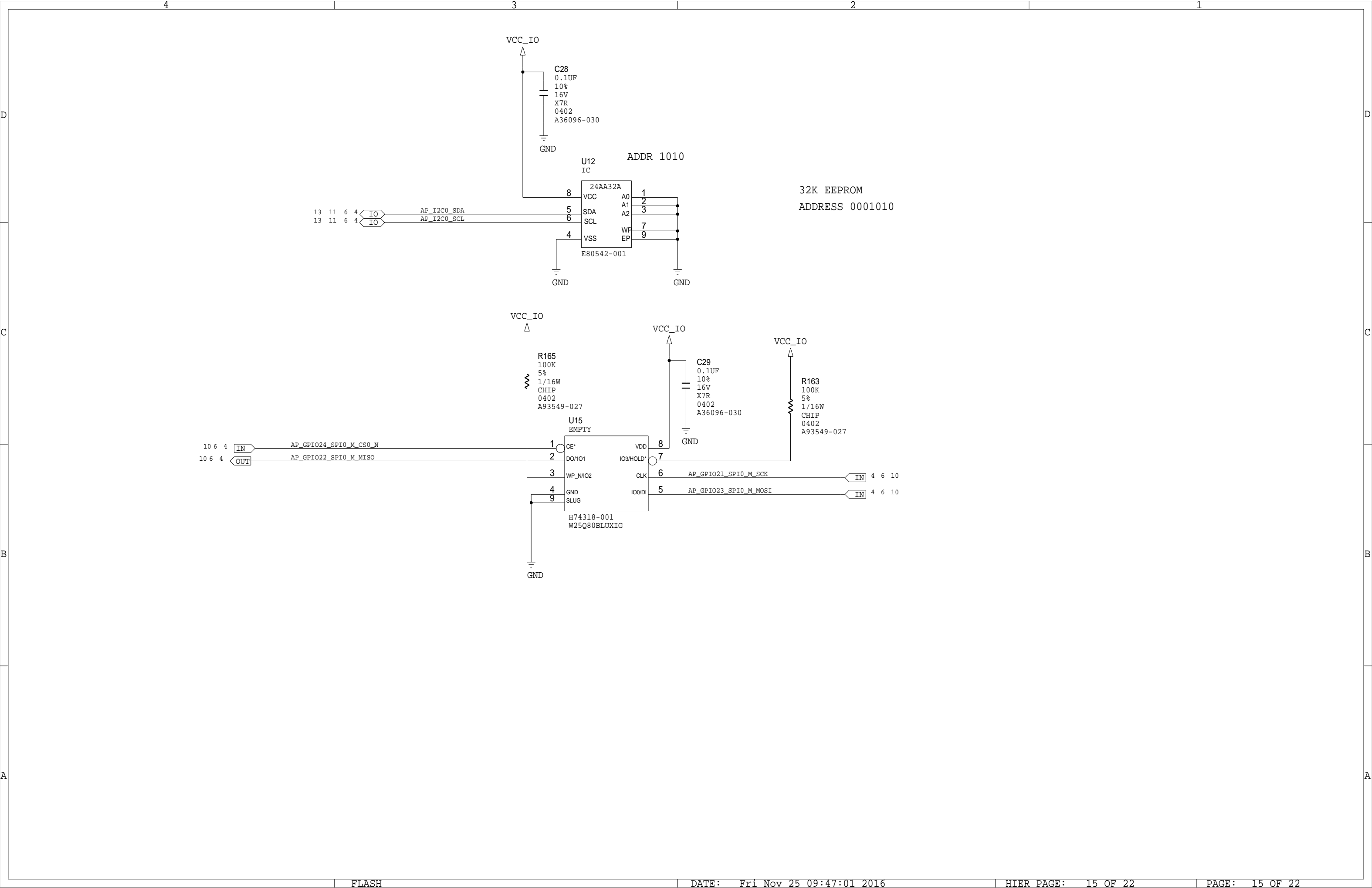
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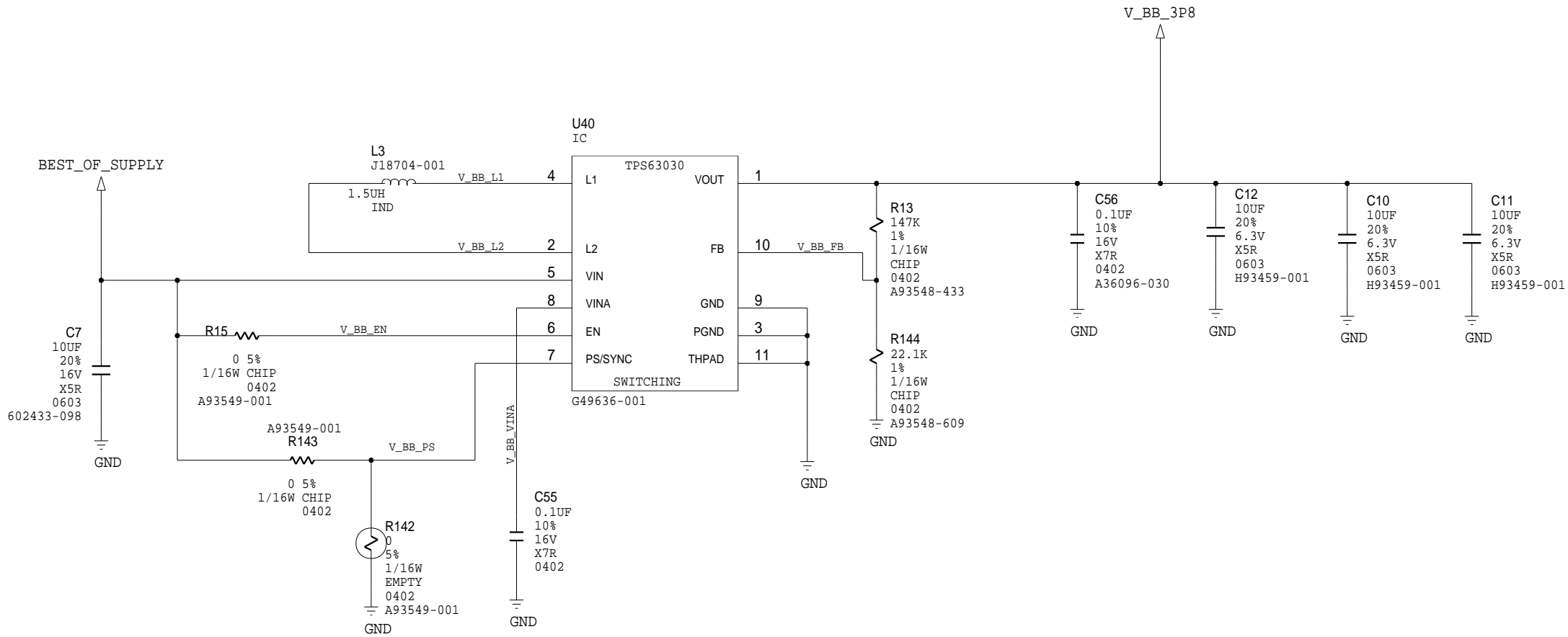
A







D
C
B
A



D
C
B
A

