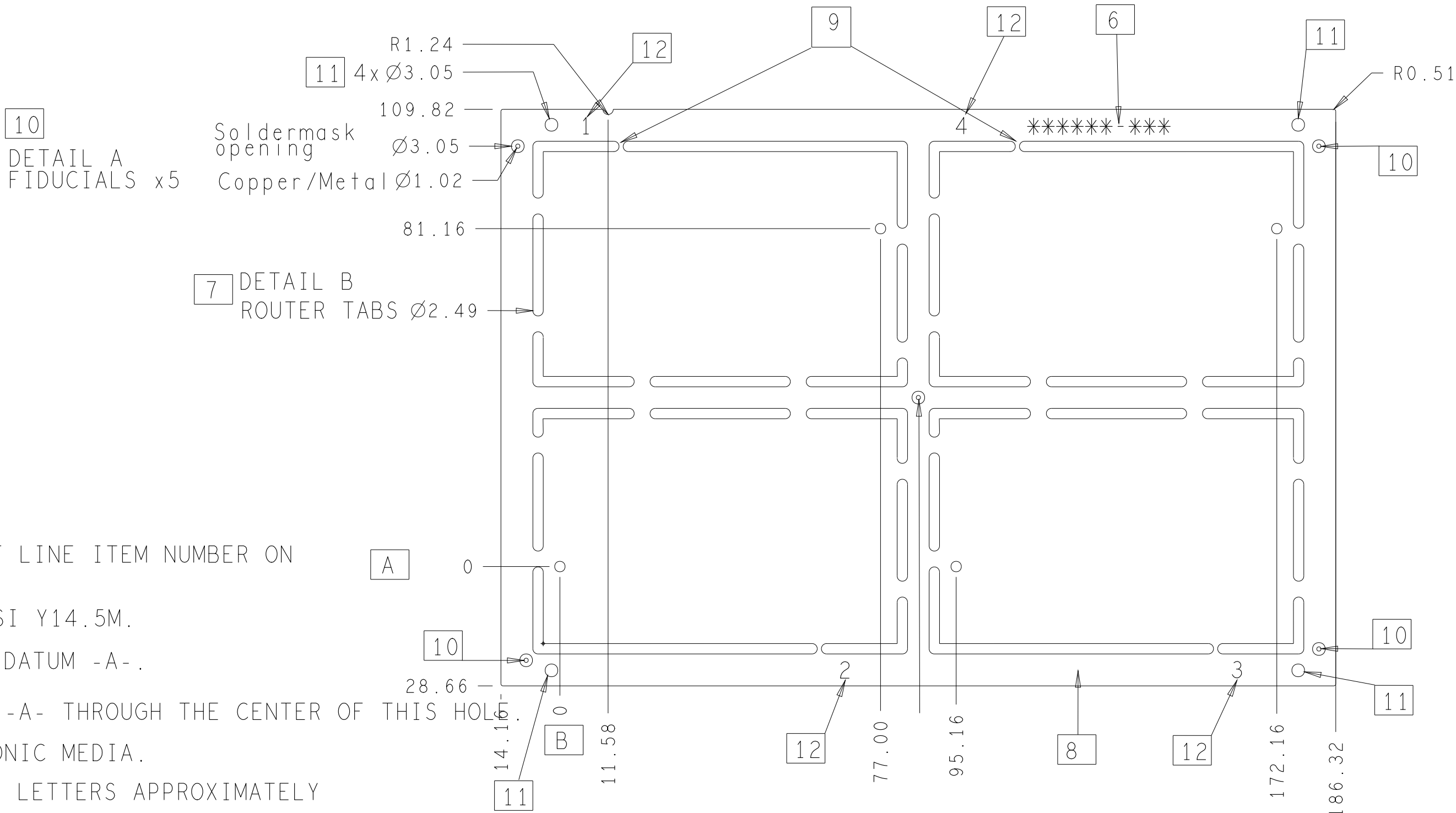



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REVISIONS						
REV	DESCRIPTION	DFT	DATE	CHK	DATE	APVD
01	INITIAL RELEASE		10/18/16			



NOTES: UNLESS OTHERWISE SPECIFIED:

1. ITEM IDENTIFICATION NUMBER IS PARENT LINE ITEM NUMBER ON ENGINEERING BILL OF MATERIAL.
2. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M.
3. THE CENTER OF THESE HOLES ESTABLISH DATUM -A-.
4. DATUM -B- IS PERPENDICULAR TO DATUM -A- THROUGH THE CENTER OF THIS HOLE.
5. THIS DOCUMENT ALSO EXISTS ON ELECTRONIC MEDIA.
6. MARK "MP" IDENTIFICATION NUMBER, AND LETTERS APPROXIMATELY WHERE SHOWN ON PRIMARY SIDE.
7. TYPICAL ROUTE SLOTS 2.49.
8. OUTRIGGER (WASTE) MATERIAL TO CONTAIN FULL METALIZATION WITH THE EXCEPTION OF THE POST-ASSEMBLY 2.49MM ROUTE PATHS. THE POST ASSEMBLY ROUTE PATHS CONSISTS OF THE 2.49MM PERIMETER SURROUNDING THE SINGLE IMAGES.
9. THESE KIND OF AREAS TO BE FREE OF METALIZATION.
10. FIDUCIAL TARGETS PER DETAIL "A" ON BOTH PRIMARY AND SECONDARY SIDES AS INDICATED.
11. ADD 3.05MM +0.05 / -0.05 NON-PLATED TOOLING HOLES x4
12. MARK IMAGE #(1, 2, 3, 4) ON OUTRIGGER TOP & BOTTOM SOLDERMASK APPROXIMATELY WHERE SHOWN.

UNLESS OTHERWISE SPECIFIED: 1. DIMENSIONS ARE IN MILLIMETERS 2. BREAK-ALL SHARP EDGES 3. DO NOT SCALE DRAWING 4. TOLERANCES ANGLES XXX XXX	SIGNATURE		DATE		 INTEL CONFIDENTIAL 3065 BOWERS AVE SANTA CLARA CALIF. 95051						
	DRN BY MIG Design Team		10/18/16								
	CHK BY				TITLE PB,MP,FAB DWG, GT96B, FAB 4						
	ENGR										
	APVD										
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				DCLC		CLASS CODE		DOCUMENT NUMBER X X X X X X - X X X		REV 01	
				SCALE 1:1				SHEET 1 OF 1			