EISA system configuration files

For reference, the following system configuration files are outlined in full for the Stallion Technologies EISA board types listed below. Actual configuration files are supplied on the EasyUtils diskette.

Contents

Creating configuration files

Configuration files can be created from the material supplied in this document, by performing the following steps:

1. Find the appropriate configuartion file from the list above.
2. Select Text mode in Acrobat or Exchange (indicated by an ‘abc’ in the toolbar). If using the PDF reader via a web browser (as a plug-in), text mode may not be available. If it is not, save this PDF file to your local drive, and open the PDF file again using Acrobat or Exchange independently of the web browser to gain access to the text function.
3. Highlight the text shown in `Courier typeface`. If text to be copied extends across more than one page, repeat steps 3, 4 and 6 as required.
4. From the ‘Edit’ menu, select ‘Copy’ (or press Control C).
5. Open a text editor (i.e. Notepad, Write, WorPad or other).
6. Paste the copied text into a new document.
7. Save the file as an ASCII text document using the appropriate file name (i.e. !STL0100.CFG if the file for ONboard/E). Do not use an UPPER CASE file name or a ‘.txt’ suffix.

Configuration file is ready for use.

!STL0100.CFG - EISA configuration file for ONboard/E

```
BOARD
      ID = "STL0100"
      NAME = "Stallion Technologies - ONboard/E"
      MFR = "STALLION TECHNOLOGIES"
      CATEGORY = "COM"
      SLOT = EISA
      READID = yes
      AMPERAGE = 2000
      COMMENTS =
         "The installation of software specific to
         the operating system is required. See
         Stallion Technologies ONboard manual
         documentation."

IOPORT(1) = 0z001h

IOPORT(2) = 0z002h

IOPORT(3) = 0z003h

FUNCTION = "ONboard/E Configuration"
      TYPE = "COM"

   SUBFUNCTION = "Dual-Ported RAM Address"
      CHOICE = "FF000000H"
            LINK
                  MEMORY = 64k
                        ADDRESS = 0ff000000h
                        MEMTYPE = oth
                        SHARE = "STL0100"
                        SIZE = dword
                  INIT = IOPORT(1) LOC(7 6 5 4 3 2 1 0) 00000000
                  INIT = IOPORT(3) LOC(7 6 5 4 3 2 1 0) 11111111
      CHOICE = "FF010000H"
            LINK
                  MEMORY = 64k
                        ADDRESS = 0ff010000h
                        MEMTYPE = oth
                        SHARE = "STL0100"
                        SIZE = dword
                  INIT = IOPORT(1) LOC(7 6 5 4 3 2 1 0) 00000001
                  INIT = IOPORT(3) LOC(7 6 5 4 3 2 1 0) 11111111
      CHOICE = "FF020000H"
            LINK
                  MEMORY = 64k
                        ADDRESS = 0ff020000h
                        MEMTYPE = oth
                        SHARE = "STL0100"
                        SIZE = dword
                  INIT = IOPORT(1) LOC(7 6 5 4 3 2 1 0) 00000010
                  INIT = IOPORT(3) LOC(7 6 5 4 3 2 1 0) 11111111
      CHOICE = "FF030000H"
            LINK
                  MEMORY = 64k
                        ADDRESS = 0ff030000h
                        MEMTYPE = oth
                        SHARE = "STL0100"
                        SIZE = dword
                  INIT = IOPORT(1) LOC(7 6 5 4 3 2 1 0) 00000011
                  INIT = IOPORT(3) LOC(7 6 5 4 3 2 1 0) 11111111
      CHOICE = "C0000H"
            LINK
                  MEMORY = 64k
                        ADDRESS = 0c0000h
```

```
                        MEMTYPE = oth
                        SHARE = "STL0100"
                        SIZE = dword
                  INIT = IOPORT(1) LOC(7 6 5 4 3 2 1 0) 00001100
                  INIT = IOPORT(3) LOC(7 6 5 4 3 2 1 0) 00000000
      CHOICE = "D0000H"
            LINK
                  MEMORY = 64k
                        ADDRESS = 0d0000h
                        MEMTYPE = oth
                        SHARE = "STL0100"
                        SIZE = dword
                  INIT = IOPORT(1) LOC(7 6 5 4 3 2 1 0) 00001101
                  INIT = IOPORT(3) LOC(7 6 5 4 3 2 1 0) 00000000
      CHOICE = "E0000H"
            LINK
                  MEMORY = 64k
                        ADDRESS = 0e0000h
                        MEMTYPE = oth
                        SHARE = "STL0100"
                        SIZE = dword
                  INIT = IOPORT(1) LOC(7 6 5 4 3 2 1 0) 00001110
                  INIT = IOPORT(3) LOC(7 6 5 4 3 2 1 0) 00000000

   SUBFUNCTION = "Interrupt Request"
      CHOICE = "IRQ 15"
            LINK
                  IRQ = 15
                  INIT = IOPORT(2) LOC(7 6 5 4 3 2 1 0) 00011001
      CHOICE = "IRQ 12"
            LINK
                  IRQ = 12
                  INIT = IOPORT(2) LOC(7 6 5 4 3 2 1 0) 00001001
      CHOICE = "IRQ 11"
            LINK
                  IRQ = 11
                  INIT = IOPORT(2) LOC(7 6 5 4 3 2 1 0) 00010001
      CHOICE = "IRQ 10"
            LINK
                  IRQ = 10
                  INIT = IOPORT(2) LOC(7 6 5 4 3 2 1 0) 00000001
      CHOICE = "IRQ 7"
            LINK
                  IRQ = 7
                  INIT = IOPORT(2) LOC(7 6 5 4 3 2 1 0) 00111001
      CHOICE = "IRQ 5"
            LINK
                  IRQ = 5
                  INIT = IOPORT(2) LOC(7 6 5 4 3 2 1 0) 00101001
      CHOICE = "IRQ 4"
            LINK
                  IRQ = 4
                  INIT = IOPORT(2) LOC(7 6 5 4 3 2 1 0) 00110001
      CHOICE = "IRQ 3"
            LINK
                  IRQ = 3
                  INIT = IOPORT(2) LOC(7 6 5 4 3 2 1 0) 00100001
```

!STL0120.CFG - EISA configuration file for ONboard (ISA)

```
BOARD
  ID = "STL0120"
  NAME = "Stallion Technologies - ONboard ISA"
  MFR = "Stallion Technologies"
  CATEGORY = "COM"
  SLOT = ISA16
  READID = no
  AMPERAGE = 2000
  COMMENTS = "
    The installation of software specific to the operating system is
required.
    See Stallion Technologies documentation.
  "

IOPORT(1) = PORTVAR(1)
IOPORT(2) = PORTVAR(2)
  INITVAL = LOC(7 6 2 0) 0000


SWITCH(1) = 4
  NAME = "I/O Address Select"
  STYPE = DIP
  VERTICAL = NO
  REVERSE = YES
  FACTORY = LOC(1-4) 1011


FUNCTION = "ONboard Configuration"
  TYPE = "COM,SER"

  SUBFUNCTION = "I/O Address"
    HELP = "I/O address of board selected by DIP switch"

    CHOICE = "200"
      PORTVAR(1) = 201h
      PORTVAR(2) = 202h
      LINK
        PORT = 200H-20Fh
          SHARE = NO
          SIZE = BYTE
        INIT = SWITCH(1) LOC(1-4) 1111

    CHOICE = "210"
      PORTVAR(1) = 211h
      PORTVAR(2) = 212h
      LINK
        PORT = 210H-21Fh
          SHARE = NO
          SIZE = BYTE
        INIT = SWITCH(1) LOC(1-4) 1110

    CHOICE = "220"
      PORTVAR(1) = 221h
      PORTVAR(2) = 222h
      LINK
        PORT = 220H-22Fh
          SHARE = NO
          SIZE = BYTE
        INIT = SWITCH(1) LOC(1-4) 1101

    CHOICE = "230"
      PORTVAR(1) = 231h
      PORTVAR(2) = 232h
      LINK
```

```
      PORT = 230H-23Fh
        SHARE = NO
        SIZE = BYTE
      INIT = SWITCH(1) LOC(1-4) 1100

  CHOICE = "240"
    PORTVAR(1) = 241h
    PORTVAR(2) = 242h
    LINK
      PORT = 240H-24Fh
        SHARE = NO
        SIZE = BYTE
      INIT = SWITCH(1) LOC(1-4) 1011

  CHOICE = "250"
    PORTVAR(1) = 251h
    PORTVAR(2) = 252h
    LINK
      PORT = 250H-25Fh
        SHARE = NO
        SIZE = BYTE
      INIT = SWITCH(1) LOC(1-4) 1010

  CHOICE = "260"
    PORTVAR(1) = 261h
    PORTVAR(2) = 262h
    LINK
      PORT = 260H-26Fh
        SHARE = NO
        SIZE = BYTE
      INIT = SWITCH(1) LOC(1-4) 1001

  CHOICE = "270"
    PORTVAR(1) = 271h
    PORTVAR(2) = 272h
    LINK
      PORT = 270H-27Fh
        SHARE = NO
        SIZE = BYTE
      INIT = SWITCH(1) LOC(1-4) 1000

  CHOICE = "280"
    PORTVAR(1) = 218h
    PORTVAR(2) = 282h
    LINK
      PORT = 280H-28Fh
        SHARE = NO
        SIZE = BYTE
      INIT = SWITCH(1) LOC(1-4) 0111

  CHOICE = "290"
    PORTVAR(1) = 291h
    PORTVAR(2) = 292h
    LINK
      PORT = 290H-29Fh
        SHARE = NO
        SIZE = BYTE
      INIT = SWITCH(1) LOC(1-4) 0110

  CHOICE = "2A0"
    PORTVAR(1) = 2A1h
    PORTVAR(2) = 2A2h
    LINK
      PORT = 2A0H-2AFh
        SHARE = NO
        SIZE = BYTE
      INIT = SWITCH(1) LOC(1-4) 0101

  CHOICE = "2B0"
```

```
      PORTVAR(1) = 2B1h
      PORTVAR(2) = 2B2h
      LINK
        PORT = 2B0H-2BFh
          SHARE = NO
          SIZE = BYTE
        INIT = SWITCH(1) LOC(1-4) 0100

    CHOICE = "2C0"
      PORTVAR(1) = 2C1h
      PORTVAR(2) = 2C2h
      LINK
        PORT = 2C0H-2CFh
          SHARE = NO
          SIZE = BYTE
        INIT = SWITCH(1) LOC(1-4) 0011

    CHOICE = "2D0"
      PORTVAR(1) = 2D1h
      PORTVAR(2) = 2D2h
      LINK
        PORT = 2D0H-2DFh
          SHARE = NO
          SIZE = BYTE
        INIT = SWITCH(1) LOC(1-4) 0010

    CHOICE = "2E0"
      PORTVAR(1) = 2E1h
      PORTVAR(2) = 2E2h
      LINK
        PORT = 2E0H-2EFh
          SHARE = NO
          SIZE = BYTE
        INIT = SWITCH(1) LOC(1-4) 0001

    CHOICE = "2F0"
      PORTVAR(1) = 2F1h
      PORTVAR(2) = 2F2h
      LINK
        PORT = 2F0H-2FFh
          SHARE = NO
          SIZE = BYTE
        INIT = SWITCH(1) LOC(1-4) 0000


  SUBFUNCTION = "Dual-Ported RAM Address"

    CHOICE = "Between A0000 and F0000"
      HELP = "Supported by OS/2, Novell AIO & NAS, and DOS"
      LINK
        MEMORY = 64K
          INIT = IOPORT(2) LOC(1) 0
          ADDRESS = 0A0000h-0f0000h STEP = 64K
          MEMTYPE = OTH
          DECODE = 20
              SIZE = BYTE
          SHARE = "STL0120"
        INIT = IOPORT(1) LOC(7-0) 00001010-00001111

    CHOICE = "Between 1Meg and 16Meg"
      HELP = "Supported by OS/2 and Novell AIO & NAS"
      LINK
        MEMORY = 64K
          INIT = IOPORT(2) LOC(1) 1
          ADDRESS = 000100000h-000ff0000h STEP = 64K
          MEMTYPE = OTH
          DECODE = 24
              SIZE = BYTE
          SHARE = "STL0120"
```

```
        INIT = IOPORT(1) LOC(7-0) 00010000-11111111


  SUBFUNCTION = "Interrupt Request"

    CHOICE = "Enable: Edge Trigger"
      HELP = "Interrupts are used. (OS/2)"
      LINK
        IRQ = 15 | 12 | 11 | 10 | 7 | 5 | 4 | 3
          TRIGGER = EDGE
        INIT = IOPORT(2) LOC(5-3) 011 | 001 | 010 | 000 | 111 | 101 | 110 | 100

    CHOICE = "Disable"
      HELP = "Interrupts are not used. (DOS, Novell AIO & NAS)"
```

!STL0130.CFG - EISA configuration file for BRUMBY

```
BOARD
  ID = "STL0130"
  NAME = "Stallion Technologies - Brumby ISA"
  MFR = "Stallion Technologies"
  CATEGORY = "COM"
  SLOT = ISA16
  READID = no
  AMPERAGE = 2000
  COMMENTS = "
    The installation of software specific to the operating
    system is required. See Stallion Technologies documentation.
  "

SWITCH(1) = 8
  NAME = "I/O & RAM Select"
  STYPE = DIP
  VERTICAL = NO
  REVERSE = YES
  FACTORY = LOC(1-8) 10010110
  HELP = "
        This Switch selects the Brumby's I/O address (300 - 3f0) and
Dual Port
        RAM address (C0000 - DC000)
  "

JUMPER(1) = 8
  NAME = "J3 - IRQ Select"
  JTYPE = PAIRED
  VERTICAL = NO
  REVERSE = YES
  LABEL = LOC(1-8) "15" "12" "11" "10" "7" "5" "4" "3"
  FACTORY = LOC(1-8) 10000000
  HELP = "
        This Jumper (J3) selects the Brumby's IRQ.
  "

FUNCTION = "Brumby Configuration"
  TYPE = "COM,SER"

  SUBFUNCTION = "I/O Address"
    HELP = "I/O address of board selected by DIP switch"

    CHOICE = "300"
      LINK
        PORT = 300H-30Fh
          SHARE = NO
          SIZE = BYTE
        INIT = SWITCH(1) LOC(1-4) 1111

    CHOICE = "310"
      LINK
        PORT = 310H-31Fh
          SHARE = NO
          SIZE = BYTE
        INIT = SWITCH(1) LOC(1-4) 1110

    CHOICE = "320"
      LINK
        PORT = 320H-32Fh
          SHARE = NO
          SIZE = BYTE
        INIT = SWITCH(1) LOC(1-4) 1101

    CHOICE = "330"
```

```
  LINK
    PORT = 330H-33Fh
      SHARE = NO
      SIZE = BYTE
    INIT = SWITCH(1) LOC(1-4) 1100

CHOICE = "340"
  LINK
    PORT = 340H-34Fh
      SHARE = NO
      SIZE = BYTE
    INIT = SWITCH(1) LOC(1-4) 1011

CHOICE = "350"
  LINK
    PORT = 350H-35Fh
      SHARE = NO
      SIZE = BYTE
    INIT = SWITCH(1) LOC(1-4) 1010

CHOICE = "360"
  LINK
    PORT = 360H-36Fh
      SHARE = NO
      SIZE = BYTE
    INIT = SWITCH(1) LOC(1-4) 1001

CHOICE = "370"
  LINK
    PORT = 370H-37Fh
      SHARE = NO
      SIZE = BYTE
    INIT = SWITCH(1) LOC(1-4) 1000

CHOICE = "380"
  LINK
    PORT = 380H-38Fh
      SHARE = NO
      SIZE = BYTE
    INIT = SWITCH(1) LOC(1-4) 0111

CHOICE = "390"
  LINK
    PORT = 390H-39Fh
      SHARE = NO
      SIZE = BYTE
    INIT = SWITCH(1) LOC(1-4) 0110

CHOICE = "3A0"
  LINK
    PORT = 3A0H-3AFh
      SHARE = NO
      SIZE = BYTE
    INIT = SWITCH(1) LOC(1-4) 0101

CHOICE = "3B0"
  LINK
    PORT = 3B0H-3BFh
      SHARE = NO
      SIZE = BYTE
    INIT = SWITCH(1) LOC(1-4) 0100

CHOICE = "3C0"
  LINK
    PORT = 3C0H-3CFh
      SHARE = NO
      SIZE = BYTE
    INIT = SWITCH(1) LOC(1-4) 0011
```

```
    CHOICE = "3D0"
      LINK
        PORT = 3D0H-3DFh
          SHARE = NO
          SIZE = BYTE
        INIT = SWITCH(1) LOC(1-4) 0010

    CHOICE = "3E0"
      LINK
        PORT = 3E0H-3EFh
          SHARE = NO
          SIZE = BYTE
        INIT = SWITCH(1) LOC(1-4) 0001

    CHOICE = "3F0"
      LINK
        PORT = 3F0H-3FFh
          SHARE = NO
          SIZE = BYTE
        INIT = SWITCH(1) LOC(1-4) 0000


  SUBFUNCTION = "Dual-Ported RAM Address"
    HELP = "IRQ of Brumby selected by DIP Switch.  Supported by OS/2 and
DOS."

    CHOICE = "C0000"
      LINK
        MEMORY = 16K
          ADDRESS = 0C0000h
          MEMTYPE = OTH
          DECODE = 20
          SIZE = BYTE
          SHARE = NO
        INIT = SWITCH(1) LOC(5-7) 111

    CHOICE = "C4000"
      LINK
        MEMORY = 16K
          ADDRESS = 0C4000h
          MEMTYPE = OTH
          DECODE = 20
          SIZE = BYTE
          SHARE = NO
        INIT = SWITCH(1) LOC(5-7) 110

    CHOICE = "C8000"
      LINK
        MEMORY = 16K
          ADDRESS = 0C8000h
          MEMTYPE = OTH
          DECODE = 20
          SIZE = BYTE
          SHARE = NO
        INIT = SWITCH(1) LOC(5-7) 101

    CHOICE = "CC000"
      LINK
        MEMORY = 16K
          ADDRESS = 0CC000h
          MEMTYPE = OTH
          DECODE = 20
          SIZE = BYTE
          SHARE = NO
        INIT = SWITCH(1) LOC(5-7) 100

    CHOICE = "D0000"
      LINK
        MEMORY = 16K
```

```
          ADDRESS = 0D0000h
          MEMTYPE = OTH
          DECODE = 20
          SIZE = BYTE
          SHARE = NO
        INIT = SWITCH(1) LOC(5-7) 011

    CHOICE = "D4000"
      LINK
        MEMORY = 16K
          ADDRESS = 0D4000h
          MEMTYPE = OTH
          DECODE = 20
          SIZE = BYTE
          SHARE = NO
        INIT = SWITCH(1) LOC(5-7) 010

    CHOICE = "D8000"
      LINK
        MEMORY = 16K
          ADDRESS = 0D8000h
          MEMTYPE = OTH
          DECODE = 20
          SIZE = BYTE
          SHARE = NO
        INIT = SWITCH(1) LOC(5-7) 001

    CHOICE = "DC000"
      LINK
        MEMORY = 16K
          ADDRESS = 0DC000h
          MEMTYPE = OTH
          DECODE = 20
          SIZE = BYTE
          SHARE = NO
        INIT = SWITCH(1) LOC(5-7) 000

  SUBFUNCTION = "Interrupt Request"
    HELP = "IRQ of Brumby selected by Jumper"

    CHOICE = "Enable"
      HELP = "Interrupts are used. (OS/2)"
      LINK
        IRQ = 15 | 12 | 11 | 10 | 7 | 5 | 4 | 3
          TRIGGER = EDGE
       INIT = JUMPER(1) LOC(1-8) 10000000 | 01000000 | 00100000 | 00010000 |
                         00001000 | 00000100 | 00000010 | 00000001

    CHOICE = "Disable"
      HELP = "Interrupts are not used. (DOS)"
      LINK
        INIT = JUMPER(1) LOC(1-8) 00000000
```

!STL0200.CFG - EISA configuration file for EasyIO (ISA)

```
BOARD
      ID = "STL0200"
      NAME = "Stallion Technologies - EasyIO"
      MFR = "STALLION TECHNOLOGIES"
      CATEGORY = "COM"
      SLOT = ISA16
      READID = no
      AMPERAGE = 2000
      COMMENTS =
         "The installation of software specific to
         the operating system is required. See
         Stallion Technologies User Manual."

SWITCH(1) = 8
  NAME = "I/O Address Select"
  STYPE = DIP
  VERTICAL = YES
  REVERSE = YES
  FACTORY = LOC(1-8) 11010100
  INITVAL = LOC(1-8) xxxxxx00

FUNCTION = "EasyIO Configuration"

   SUBFUNCTION = "Interrupt Request"

      CHOICE = "Default (15, Level Trigger)"
            LINK
                  IRQ         = 15
                  SHARE   = "STL0200"
                  TRIGGER = LEVEL

      CHOICE = "Level Trigger"
            LINK
                  IRQ = 12 | 11 | 10 | 7 | 5 | 4 | 3
                  SHARE   = "STL0200"
                  TRIGGER = LEVEL

      CHOICE = "Edge Trigger"
            LINK
                  IRQ = 12 | 11 | 10 | 7 | 5 | 4 | 3
                  TRIGGER = EDGE

   SUBFUNCTION = "IO Address"
      CHOICE = "Default (2a0h)"
            LINK
                  PORT = 2a0h-2a7h
                  INIT = SWITCH(1) LOC(1-6) 110101

      CHOICE = "Other (200h -> 3ff)"
            LINK
                PORT = 200h-3ffh STEP=8
                INIT = SWITCH(1) LOC(1-6) 111111 | 011111 | 101111 | 001111 |
110111 | 010111 | 100111 | 000111 | 111011 | 011011 | 101011 | 001011 |
110011 | 010011 | 100011 | 000011 | 111101 | 011101 | 101101 | 001101 |
110101 | 010101 | 100101 | 000101 | 111001 | 011001 | 101001 | 001001 |
110001 | 010001 | 100001 | 000001 | 111110 | 011110 | 101110 | 001110 |
110110 | 010110 | 100110 | 000110 | 111010 | 011010 | 101010 | 001010 |
110010 | 010010 | 100010 | 000010 | 111100 | 011100 | 101100 | 001100 |
110100 | 010100 | 100100 | 000100 | 111000 | 011000 | 101000 | 001000 |
110000 | 010000 | 100000 | 000000
```

!STL0400.CFG - EISA configuration file for EC8/64-EI

```
; !stl0400.cfg - EISA configuration file for EC 8/64-EI
; author            - Peter S. Calvert
; created           -
; notes                     -
;      1)     A cludge has been made to be able to put half of the RAM address
;             in the board address+1 and the upper half in address+3.
;             As it turns out it was not needed as the board is programed again
;             by cdkh_brd.c at init time.
;             reg
;             +0
;             +1     addr lower byte  \ IOPORT(1) as a WORD
;             +2  IOPORT(3)        /          \ IOPORT(2) as a WORD
;             +3     addr upper byte            /
;      Notice IOPORT(1)'s lower byte maps onto the lower part of addr
;      Notice IOPORT(2)'s upper byte maps onto the upper part of addr
;      IOPORT(3) will be initialised after (1) and (2) so it cleans up
;      the mess left in addr +2
;
BOARD
  ID = "STL0400"
  NAME = "Stallion Technologies - EC 8/64-EI"
  MFR = "STALLION TECHNOLOGIES"
  CATEGORY = "COM"
  SLOT = EISA
  READID = yes
  AMPERAGE = 2000
  COMMENTS = "
    The installation of software specific to the operating system is required.
    See Stallion Technologies documentation.
  "

IOPORT(1) = 0z001h
       SIZE = WORD
IOPORT(2) = 0z002h
       SIZE = WORD
IOPORT(3) = 0z002h
       SIZE = BYTE
       INITVAL = LOC(7-0) x0xxx000

FUNCTION = "EC 8/64-EI Configuration"
  TYPE = "COM,SER"

  SUBFUNCTION = "Dual-Ported RAM Address"

    CHOICE = "In range (C0000000H -> FFFF0000H)"
      LINK
        MEMORY = 64K
          ADDRESS = 0c0000000h-0ffff0000h STEP = 64K
          MEMTYPE = OTH
          SHARE = "STL0400"
        INIT = IOPORT(1) LOC(15-0) 1100000000000000-1111111111111111
           INIT = IOPORT(2) LOC(15-0) 1100000000000000-1111111111111111

    CHOICE = "In range (80000000H -> BFFF0000H)"
      LINK
        MEMORY = 64K
          ADDRESS = 080000000h-0bfff0000h STEP = 64K
          MEMTYPE = OTH
          SHARE = "STL0400"
        INIT = IOPORT(1) LOC(15-0) 1000000000000000-1011111111111111
           INIT = IOPORT(2) LOC(15-0) 1000000000000000-1011111111111111

    CHOICE = "In range (40000000H -> 7FFF0000H)"
      LINK
        MEMORY = 64K
          ADDRESS = 040000000h-07fff0000h STEP = 64K
          MEMTYPE = OTH
          SHARE = "STL0400"
        INIT = IOPORT(1) LOC(15-0) 0100000000000000-0111111111111111
           INIT = IOPORT(2) LOC(15-0) 0100000000000000-0111111111111111

    CHOICE = "In range (00000000H -> 3FFF0000H)"
```

```
      LINK
        MEMORY = 64K
          ADDRESS = 000000000h-03fff0000h STEP = 64K
          MEMTYPE = OTH
          SHARE = "STL0400"
        INIT = IOPORT(1) LOC(15-0) 0000000000000000-0011111111111111
           INIT = IOPORT(2) LOC(15-0) 0000000000000000-0011111111111111


  SUBFUNCTION = "Interrupt Request"

    CHOICE = "Disable"
      HELP = "Board is polled."

    CHOICE = "Enable: Level Trigger"
      HELP = "Allows sharing of single interrupt."
      LINK
       IRQ = 15 | 12 | 11 | 10 | 7 | 5 | 4 | 3
       TRIGGER = LEVEL
          SHARE = "STL0400"
       INIT = IOPORT(3) LOC(7 5 4 3) 1011 | 1001 | 1010 | 1000 | 1111 | 1101 |
                                                              1110 | 1100

    CHOICE = "Enable: Edge Trigger"
      LINK
       IRQ = 15 | 12 | 11 | 10 | 7 | 5 | 4 | 3
       TRIGGER = EDGE
       INIT = IOPORT(3) LOC(7 5 4 3) 0011 | 0001 | 0010 | 0000 | 0111 | 0101 |
                                                            0110 | 0100
```

!STL0410.CFG - EISA configuration file for EC8/32-AT

```
BOARD
      ID = "STL0410"
      NAME = "Stallion Technologies - EC 8/32-AT"
      MFR = "STALLION TECHNOLOGIES"
      CATEGORY = "COM"
      SLOT = ISA16
      READID = no
      AMPERAGE = 2000
      COMMENTS =
         "The installation of software specific to
         the operating system is required. See
         Stallion Technologies User Manual."

SWITCH(1) = 8
  NAME = "I/O Address Select"
  STYPE = DIP
  VERTICAL = YES
  REVERSE = YES
  FACTORY = LOC(1-8) 11110101

FUNCTION = "EC 8/32-AT Configuration"

   SUBFUNCTION = "Interrupt Request"

      CHOICE = "Default (15, Level Triggered)"
            LINK
                  IRQ         = 15
                  SHARE   = "STL0410"
                  TRIGGER = LEVEL

      CHOICE = "Level Triggered (Shared)"
            LINK
                  IRQ         = 12 | 11 | 10 | 7 | 5 | 4 | 3
                  SHARE   = "STL0410"
                  TRIGGER = LEVEL

      CHOICE = "Edge Triggered (Non-Shared)"
            LINK
                  IRQ         = 15 | 12 | 11 | 10 | 7 | 5 | 4 | 3
                  TRIGGER = EDGE

   SUBFUNCTION = "Primary IO Address"

      CHOICE = "Default (2a0H)"
            LINK
                  PORT = 2a0h-2a1h
                  INIT = SWITCH(1) LOC(1-8) 11110101b

      CHOICE = "Other (200H -> 2feH)"
            LINK
                  PORT = 200h-2ffh STEP=2
                  INIT = SWITCH(1) LOC(1-8) 11111111b |
                        01111111b | 10111111b | 00111111b |
                        11011111b | 01011111b | 10011111b | 00011111b |
                        11101111b | 01101111b | 10101111b | 00101111b |
                        11001111b | 01001111b | 10001111b | 00001111b |
                        11110111b | 01110111b | 10110111b | 00110111b |
                        11010111b | 01010111b | 10010111b | 00010111b |
                        11100111b | 01100111b | 10100111b | 00100111b |
                        11000111b | 01000111b | 10000111b | 00000111b |
                        11111011b | 01111011b | 10111011b | 00111011b |
                        11011011b | 01011011b | 10011011b | 00011011b |
                        11101011b | 01101011b | 10101011b | 00101011b |
                        11001011b | 01001011b | 10001011b | 00001011b |
```

```
                        11110011b | 01110011b | 10110011b | 00110011b |
                        11010011b | 01010011b | 10010011b | 00010011b |
                        11100011b | 01100011b | 10100011b | 00100011b |
                        11000011b | 01000011b | 10000011b | 00000011b |
                        11111101b | 01111101b | 10111101b | 00111101b |
                        11011101b | 01011101b | 10011101b | 00011101b |
                        11101101b | 01101101b | 10101101b | 00101101b |
                        11001101b | 01001101b | 10001101b | 00001101b |
                        11110101b | 01110101b | 10110101b | 00110101b |
                        11010101b | 01010101b | 10010101b | 00010101b |
                        11100101b | 01100101b | 10100101b | 00100101b |
                        11000101b | 01000101b | 10000101b | 00000101b |
                        11111001b | 01111001b | 10111001b | 00111001b |
                        11011001b | 01011001b | 10011001b | 00011001b |
                        11101001b | 01101001b | 10101001b | 00101001b |
                        11001001b | 01001001b | 10001001b | 00001001b |
                        11110001b | 01110001b | 10110001b | 00110001b |
                        11010001b | 01010001b | 10010001b | 00010001b |
                        11100001b | 01100001b | 10100001b | 00100001b |
                        11000001b | 01000001b | 10000001b | 00000001b

      CHOICE = "Other (300H -> 3feH)"
            LINK
                  PORT = 300h-3ffh STEP=2
                  INIT = SWITCH(1) LOC(1-8) 11111110b |
                        01111110b | 10111110b | 00111110b |
                        11011110b | 01011110b | 10011110b | 00011110b |
                        11101110b | 01101110b | 10101110b | 00101110b |
                        11001110b | 01001110b | 10001110b | 00001110b |
                        11110110b | 01110110b | 10110110b | 00110110b |
                        11010110b | 01010110b | 10010110b | 00010110b |
                        11100110b | 01100110b | 10100110b | 00100110b |
                        11000110b | 01000110b | 10000110b | 00000110b |
                        11111010b | 01111010b | 10111010b | 00111010b |
                        11011010b | 01011010b | 10011010b | 00011010b |
                        11101010b | 01101010b | 10101010b | 00101010b |
                        11001010b | 01001010b | 10001010b | 00001010b |
                        11110010b | 01110010b | 10110010b | 00110010b |
                        11010010b | 01010010b | 10010010b | 00010010b |
                        11100010b | 01100010b | 10100010b | 00100010b |
                        11000010b | 01000010b | 10000010b | 00000010b |
                        11111100b | 01111100b | 10111100b | 00111100b |
                        11011100b | 01011100b | 10011100b | 00011100b |
                        11101100b | 01101100b | 10101100b | 00101100b |
                        11001100b | 01001100b | 10001100b | 00001100b |
                        11110100b | 01110100b | 10110100b | 00110100b |
                        11010100b | 01010100b | 10010100b | 00010100b |
                        11100100b | 01100100b | 10100100b | 00100100b |
                        11000100b | 01000100b | 10000100b | 00000100b |
                        11111000b | 01111000b | 10111000b | 00111000b |
                        11011000b | 01011000b | 10011000b | 00011000b |
                        11101000b | 01101000b | 10101000b | 00101000b |
                        11001000b | 01001000b | 10001000b | 00001000b |
                        11110000b | 01110000b | 10110000b | 00110000b |
                        11010000b | 01010000b | 10010000b | 00010000b |
                        11100000b | 01100000b | 10100000b | 00100000b |
                        11000000b | 01000000b | 10000000b | 00000000b

   SUBFUNCTION = "Secondary IO Address"
      CHOICE = "Default (280H)"
            LINK
                  PORT = 280h-29fh
                  SHARE = "STL0410"
      CHOICE = "Other (200H -> 3e0H)"
            LINK
                  PORT = 200h-3ffh STEP=20H
                  SHARE = "STL0410"
```

!STL0420.CFG - EISA configuration file for EC8/64-AT

```
BOARD
  ID = "STL0420"
  NAME = "Stallion Technologies - EC 8/64-AT"
  MFR = "STALLION TECHNOLOGIES"
  CATEGORY = "COM"
  SLOT = ISA16
  READID = no
  AMPERAGE = 2000
  COMMENTS = "
    The installation of software specific to the operating system
    is required. See Stallion Technologies documentation.
  "

SWITCH(1) = 8
  NAME = "I/O Address Select"
  STYPE = DIP
  VERTICAL = YES
  REVERSE = YES
  FACTORY = LOC(1-8) 11101010
  INITVAL = LOC(1-8) xxxxxxx0

FUNCTION = "EC 8/64-AT Configuration"
  TYPE = "COM,SER"

  SUBFUNCTION = "Dual-Ported RAM Address"
    CHOICE = "Default (D0000)"
      FREE
        MEMORY = 4K
          ADDRESS = 0d0000h
          MEMTYPE = OTH
          SHARE = "STL0420"

    CHOICE = "Other (C0000 -> FF000)"
      LINK
        MEMORY = 4K
          ADDRESS = 0c0000h-0ff000h STEP = 4K
          MEMTYPE = OTH
          SHARE = "STL0420"

  SUBFUNCTION = "I/O Address"
    CHOICE = "Default (2A0H)"
      LINK
        PORT = 2a0H-2a3h
        INIT = SWITCH(1) LOC(1-7)      1110101

    CHOICE = "Other (200H -> 3FCH)"
     LINK
      PORT = 200H-3ffh STEP=4
      INIT = SWITCH(1) LOC(1-7)  1111111 | 0111111 | 1011111 | 0011111 |
             1101111 | 0101111 | 1001111 | 0001111 | 1110111 | 0110111 |
             1010111 | 0010111 | 1100111 | 0100111 | 1000111 | 0000111 |
             1111011 | 0111011 | 1011011 | 0011011 | 1101011 | 0101011 |
             1001011 | 0001011 | 1110011 | 0110011 | 1010011 | 0010011 |
             1100011 | 0100011 | 1000011 | 0000011 | 1111101 | 0111101 |
             1011101 | 0011101 | 1101101 | 0101101 | 1001101 | 0001101 |
             1110101 | 0110101 | 1010101 | 0010101 | 1100101 | 0100101 |
             1000101 | 0000101 | 1111001 | 0111001 | 1011001 | 0011001 |
             1101001 | 0101001 | 1001001 | 0001001 | 1110001 | 0110001 |
             1010001 | 0010001 | 1100001 | 0100001 | 1000001 | 0000001 |
             1111110 | 0111110 | 1011110 | 0011110 | 1101110 | 0101110 |
             1001110 | 0001110 | 1110110 | 0110110 | 1010110 | 0010110 |
             1100110 | 0100110 | 1000110 | 0000110 | 1111010 | 0111010 |
             1011010 | 0011010 | 1101010 | 0101010 | 1001010 | 0001010 |
             1110010 | 0110010 | 1010010 | 0010010 | 1100010 | 0100010 |
             1000010 | 0000010 | 1111100 | 0111100 | 1011100 | 0011100 |
             1101100 | 0101100 | 1001100 | 0001100 | 1110100 | 0110100 |
             1010100 | 0010100 | 1100100 | 0100100 | 1000100 | 0000100 |
             1111000 | 0111000 | 1011000 | 0011000 | 1101000 | 0101000 |
             1001000 | 0001000 | 1110000 | 0110000 | 1010000 | 0010000 |
             1100000 | 0100000 | 1000000 | 0000000

  SUBFUNCTION = "Interrupt Request"
```

```
CHOICE = "Disable"
  HELP = "Interrupts are NOT used, Board is polled."

   CHOICE = "Enable"
  HELP = "Interrupts are used, Board interrupts host."
  LINK
    IRQ = 15 | 10 | 12 | 11 | 3 | 5 | 4 | 7
    TRIGGER = EDGE
```